

ABSTRACT OF THE DISCLOSURE

A method of switching the mode of a PLL circuit which has a high-speed mode and a normal mode, which allows the  
5 PLL circuit to be locked up at a high speed. The PLL circuit includes a phase comparator and a charge pump for generating a current depending on a comparison output signal from the phase comparator. The mode switching method includes the steps of detecting whether a current  
10 output terminal of the charge pump is in a high impedance state, and switching the mode of the PLL circuit from the high-speed mode to the normal mode or from the normal mode to the high-speed mode when the high impedance state is detected.

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